

AMENDMENTS TO THE CLAIMS

Please cancel Claims 1-20, without prejudice.

Please add new Claims 21-38 as follows.

1-20. (Cancelled)

21. (New) A liquid crystal display comprising :

a first substrate;

a plurality of scan lines located on the first substrate;

a plurality of video data lines located on the first substrate and arranged to cross the scan lines, wherein any adjacent scan lines and any adjacent video data lines define a pixel region, the pixel region comprising a common electrode line; and

a plurality of pixel electrodes located at the pixel region, wherein the pixel electrode is divided into a plurality of sub pixel electrodes, a closed slit is located between adjacent sub pixel electrodes, and the portion of the common electrode line is under the closed slit.

22. (New) The liquid crystal display of claim 21, wherein the common electrode line is expanded to branch.

23. (New) The liquid crystal display of claim 22, wherein the branch of the common electrode line is parallel to the scan line.

24. (New) The liquid crystal display of claim 22, wherein the branch of the common electrode line is parallel to the video data line.

25. (New) The liquid crystal display of claim 22, wherein the branch of the common electrode line is under the closed slit.

26. (New) The liquid crystal display of claim 22, wherein the branch of the common electrode line is symmetric.

27. (New) The liquid crystal display of claim 22, wherein the portion of the branch of the common electrode line partially overlaps with the corresponding pixel electrode to form a capacitor structure.
28. (New) The liquid crystal display of claim 21, wherein the portion of the common electrode line partially overlaps with the corresponding pixel electrode to form a capacitor structure.
29. (New) The liquid crystal display of claim 21, wherein the pixel electrode is formed from an ITO or IZO material.
30. (New) A liquid crystal display comprising:
 - a first substrate;
 - a plurality of scan lines located on the first substrate;
 - a plurality of video data lines located on the first substrate and arranged to cross the scan lines, wherein any adjacent scan lines and any adjacent video data lines define a pixel region, the pixel region comprising a common electrode line; and
 - a plurality of pixel electrodes located at the pixel region, wherein the pixel electrode is divided into a plurality of sub pixel electrodes, a slit is located between adjacent sub pixel electrodes, and the slit is within the common electrode.
31. (New) The liquid crystal display of claim 30, wherein the common electrode line is expanded to branch.
32. (New) The liquid crystal display of claim 31, wherein the branch of the common electrode line is parallel to the scan line.
33. (New) The liquid crystal display of claim 31, wherein the branch of the common electrode line is parallel to the video data line.
34. (New) The liquid crystal display of claim 31, wherein the branch of the common electrode line is under the slit.

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35. (New) The liquid crystal display of claim 31, wherein the branch of the common electrode line is symmetric.
36. (New) The liquid crystal display of claim 31, wherein the portion of the branch of the common electrode line partially overlaps with the corresponding pixel electrode to form a capacitor structure.
37. (New) The liquid crystal display of claim 30, wherein the portion of the common electrode line partially overlaps with the corresponding pixel electrode to form a capacitor structure.
38. (New) The liquid crystal display of claim 30, wherein the pixel electrode is formed from an ITO or IZO material.